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Design Automation Conference, 2002. Proceedings. 39th , 10-14 June 2002

Page(s): 570 -575

[\[Abstract\]](#) [\[PDF Full-Text \(728 KB\)\]](#) **IEEE CNF****2 A parallel LCC simulation system***Hering, K.;*

Design, Automation and Test in Europe Conference and Exhibition, 2002. Proceedings , 4-8 March 2002

Page(s): 1134

[\[Abstract\]](#) [\[PDF Full-Text \(266 KB\)\]](#) **IEEE CNF****3 Performance prediction on graphics hardware using software simulation***Wong, D.W.-H.; Aleksic, M.;*

Electrical and Computer Engineering, 2001. Canadian Conference on , Volume: 2 , 13-16 May 2001

Page(s): 1235 -1240 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(484 KB\)\]](#) **IEEE CNF****4 Applying cycle-based simulation technique to VITAL as a VHDL gate level standard***Yaran, B.H.; Rahmati, D.; Zebardast, A.S.;*

Electrical and Computer Engineering, 2001. Canadian Conference on

, Volume: 2 , 13-16 May 2001

Page(s): 1079 -1084 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(372 KB\)\]](#) **IEEE CNF**

5 dIbSIM-a parallel functional logic simulator allowing dynamic load balancing

Hering, K.; Loser, J.; Markwardt, J.;

Design, Automation and Test in Europe, 2001. Conference and Exhibition 2001. Proceedings , 13-16 March 2001

Page(s): 472 -478

[\[Abstract\]](#) [\[PDF Full-Text \(540 KB\)\]](#) **IEEE CNF**

6 Exploiting timed transition relations in sequential cycle-based simulation of embedded systems

Cabodi, G.; Camurati, P.; Passerone, C.; Quer, S.;

Computers and Digital Techniques, IEE Proceedings- , Volume: 147

Issue: 5 , Sept. 2000

Page(s): 305 -312

[\[Abstract\]](#) [\[PDF Full-Text \(784 KB\)\]](#) **IEE JNL**

7 Cycle-based simulation algorithms for digital systems using high-level decision diagrams

Morawice, A.; Ubar, R.; Raik, J.;

Design, Automation and Test in Europe Conference and Exhibition 2000. Proceedings , 27-30 March 2000

Page(s): 748

[\[Abstract\]](#) [\[PDF Full-Text \(16 KB\)\]](#) **IEEE CNF**

8 Hybrid parallel circuit simulation approaches

Naroska, E.; Feipei Lai; Rung-Ji Shang; Schweigelshohn, U.;

Parallel Architectures and Compilation Techniques, 2000.

Proceedings. International Conference on , 15-19 Oct. 2000

Page(s): 261 -270

[\[Abstract\]](#) [\[PDF Full-Text \(952 KB\)\]](#) **IEEE CNF**

9 Back-tracing and event-driven techniques in high-level simulation with decision diagrams

Ubar, R.; Raik, J.; Morawiec, A.;

Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE International Symposium on , Volume: 1 , 28-31 May 2000

Page(s): 208 -211 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(364 KB\)\]](#) **IEEE CNF**

✓ 10 **Cycle-based symbolic simulation of gate-level synchronous circuits**

Bertacco, V.; Damiani, M.; Quer, S.;

Design Automation Conference, 1999. Proceedings. 36th , 21-25

June 1999

Page(s): 391 -396

[\[Abstract\]](#) [\[PDF Full-Text \(520 KB\)\]](#) **IEEE CNF**

11 **A scalable front-end architecture for fast instruction delivery**

Reinman, G.; Anstin, T.; Calder, B.;

Computer Architecture, 1999. Proceedings of the 26th International Symposium on , 2-4 May 1999

Page(s): 234 -245

[\[Abstract\]](#) [\[PDF Full-Text \(216 KB\)\]](#) **IEEE CNF**

12 **Cycle-based simulation with decision diagrams**

Ubar, R.; Morawiec, A.; Raik, J.;

Design, Automation and Test in Europe Conference and Exhibition 1999. Proceedings , 9-12 March 1999

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13 **Computing timed transition relations for sequential cycle-based simulation**

Cabodi, G.; Camurati, P.; Passerone, C.; Quer, S.;

Design, Automation and Test in Europe Conference and Exhibition 1999. Proceedings , 9-12 March 1999

Page(s): 8 -12

[\[Abstract\]](#) [\[PDF Full-Text \(220 KB\)\]](#) **IEEE CNF**

14 **Cycle-based simulation on loosely-coupled systems**

Dohler, D.; Hering, K.; Spruth, W.G.;

ASIC Conference 1998. Proceedings. Eleventh Annual IEEE International , 13-16 Sept. 1998

Page(s): 301 -305

[\[Abstract\]](#) [\[PDF Full-Text \(420 KB\)\]](#) **IEEE CNF**

15 OMI-compliant model for virtual emulation

Dozza, D.; Rambaldi, R.; Borgatti, M.; Guerrieri, R.;
Rapid System Prototyping, 1998. Proceedings. 1998 Ninth
International Workshop on , 3-5 June 1998
Page(s): 64 -69

[\[Abstract\]](#) [\[PDF Full-Text \(148 KB\)\]](#) **IEEE CNF**

**16 Top-down design using cycle based simulation: an MPEG
A/V decoder example**

Hocevar, D.E.; Ching-Yu Hung; Pickens, D.; Sriram, S.;
VLSI, 1998. Proceedings of the 8th Great Lakes Symposium on ,
19-21 Feb. 1998
Page(s): 400 -405

[\[Abstract\]](#) [\[PDF Full-Text \(40 KB\)\]](#) **IEEE CNF**

**17 Guidelines for safe simulation and synthesis of implicit
style Verilog**

Arnold, M.G.; Sample, N.J.; Shuler, J.D.;
Verilog HDL Conference and VHDL International Users Forum, 1998.
IVC/VIUF. Proceedings., 1998 International , 16-19 March 1998
Page(s): 59 -66

[\[Abstract\]](#) [\[PDF Full-Text \(208 KB\)\]](#) **IEEE CNF**

**18 Architectural simulation in the context of behavioral
synthesis**

Jemai, A.; Kission, P.; Jerraya, A.A.;
Design, Automation and Test in Europe, 1998., Proceedings , 23-26
Feb. 1998
Page(s): 590 -595

[\[Abstract\]](#) [\[PDF Full-Text \(116 KB\)\]](#) **IEEE CNF**

**19 A Conceptual Activity Cycle-based Simulation Modeling
Method**

Jingsheng Shi;
Simulation Conference Proceedings, 1997. Winter , 7-10 December
1997
Page(s): 1127 -1133

[\[Abstract\]](#) [\[PDF Full-Text \(524 KB\)\]](#) **IEEE CNF**

20 Quick generation of temporal power waveforms for RT-level hard macros

Benini, L.; De Micheli, G.; Macii, E.; Poncino, M.; Scarsi, R.;
Innovative Systems in Silicon, 1997. Proceedings., Second Annual
IEEE International Conference on , 8-10 Oct. 1997
Page(s): 331 -337

[\[Abstract\]](#) [\[PDF Full-Text \(320 KB\)\]](#) **IEEE CNF**

21 Cycle-based timing simulations using event-streams

Kei-Yong Khoo; Willson, A.N., Jr.;
Computer Design: VLSI in Computers and Processors, 1996. ICCD
'96. Proceedings., 1996 IEEE International Conference on , 7-9 Oct.
1996
Page(s): 460 -465

[\[Abstract\]](#) [\[PDF Full-Text \(672 KB\)\]](#) **IEEE CNF**

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Telecommunications Energy Conference, 2002. INTELEC. 24th Annual International , 29 Sept.-3 Oct. 2002

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[\[Abstract\]](#) [\[PDF Full-Text \(404 KB\)\]](#) **IEEE CNF****2 A new process-variation-immunity method for extracting capacitance coupling coefficients in flash memory cells***Caleb Yu-Sheng Cho; Ming-Jer Chen; Jia-Han Lin; Chiou-Feng Chen;*

Electron Device Letters, IEEE , Volume: 23 Issue: 7 , July 2002

Page(s): 422 -424

[\[Abstract\]](#) [\[PDF Full-Text \(199 KB\)\]](#) **IEEE JNL****3 Substrate-induced high-frequency noise in deep sub-micron MOSFETs for RF applications***Kishore, S.V.; Chang, G.; Asmanis, G.; Hull, C.; Stubbe, F.;*

Custom Integrated Circuits, 1999. Proceedings of the IEEE 1999 , 16-19 May 1999

Page(s): 365 -368

[\[Abstract\]](#) [\[PDF Full-Text \(316 KB\)\]](#) **IEEE CNF****4 Optimal conditions for Boolean and current detection of**

floating gate faults

Renovell, M.; Ivanov, A.; Bertrand, Y.; Azais, F.; Rafiq, S.;
Test Conference, 1999. Proceedings. International , 28-30 Sept.
1999
Page(s): 477 -486

[\[Abstract\]](#) [\[PDF Full-Text \(1140 KB\)\]](#) **IEEE CNF**

5 Substrate noise coupling through planar spiral inductor

Pun, A.L.L.; Yeung, T.; Lau, J.; Clement, J.R.; Su, D.K.;
Solid-State Circuits, IEEE Journal of , Volume: 33 Issue: 6 , June
1998
Page(s): 877 -884

[\[Abstract\]](#) [\[PDF Full-Text \(284 KB\)\]](#) **IEEE JNL**

6 Experimental results and simulation of substrate noise coupling via planar spiral inductor in RF ICs

Pun, A.; Yeung, T.; Lau, J.; Clement, F.J.R.; Su, D.;
Electron Devices Meeting, 1997. Technical Digest., International ,
7-10 Dec. 1997
Page(s): 325 -328

[\[Abstract\]](#) [\[PDF Full-Text \(368 KB\)\]](#) **IEEE CNF**

7 Test strategy sensitivity to defect parameters

Renovell, M.; Bertrand, Y.;
Test Conference, 1997. Proceedings., International , 1-6 Nov. 1997
Page(s): 607 -616

[\[Abstract\]](#) [\[PDF Full-Text \(956 KB\)\]](#) **IEEE CNF**

8 Packaging and power distribution design considerations for a Sun Microsystems desktop workstation

Smith, L.D.;
Electrical Performance of Electronic Packaging, 1997., IEEE 6th
Topical Meeting on , 27-29 Oct. 1997
Page(s): 19 -22

[\[Abstract\]](#) [\[PDF Full-Text \(308 KB\)\]](#) **IEEE CNF**

9 Test strategy sensitivity to floating gate fault parameter

Renovell, M.; Bertrand, Y.; Azais, F.;
Innovative Systems in Silicon, 1997. Proceedings., Second Annual

IEEE International Conference on , 8-10 Oct. 1997

Page(s): 186 -195

[\[Abstract\]](#) [\[PDF Full-Text \(504 KB\)\]](#) **IEEE CNF**

10 Current-mode differential logic circuits for low power digital systems

Martin, M.N.; Pouliquen, P.O.; Andreou, A.G.; Fraeman, M.E.;
Circuits and Systems, 1996., IEEE 39th Midwest symposium on ,
Volume: 1 , 18-21 Aug. 1996
Page(s): 183 -186 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(368 KB\)\]](#) **IEEE CNF**

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*Zhang, M.T.;*Electrical Performance of Electronic Packaging, 2001 , 29-31 Oct.
2001

Page(s): 215 -218

[\[Abstract\]](#) [\[PDF Full-Text \(321 KB\)\]](#) **IEEE CNF****52 A fuzzy-logic-based AVR for a stand-alone alternator feeding a heater load***Awadallah, M.A.; Morcos, M.M.;*

Power Engineering Review, IEEE , Volume: 21 Issue: 10 , Oct. 2001

Page(s): 53 -56

[\[Abstract\]](#) [\[PDF Full-Text \(318 KB\)\]](#) **IEEE JNL****53 AC resistance of planar power inductors and the quasidistributed gap technique***Jiankun Hu; Sullivan, C.R.;*

Power Electronics, IEEE Transactions on , Volume: 16 Issue: 4 , July 2001

Page(s): 558 -567

[\[Abstract\]](#) [\[PDF Full-Text \(212 KB\)\]](#) **IEEE JNL****54 A continually online trained neurocontroller for excitation and turbine control of a turbogenerator**

Venayagamoorthy, G.K.; Harley, R.G.;
Energy Conversion, IEEE Transactions on , Volume: 16 Issue: 3 ,
Sept. 2001
Page(s): 261 -269

[\[Abstract\]](#) [\[PDF Full-Text \(232 KB\)\]](#) **IEEE JNL**

55 A multimodule parallelable series-connected PWM voltage regulator

Chiang, S.J.; Yen, C.Y.; Chang, K.T.;
Industrial Electronics, IEEE Transactions on , Volume: 48 Issue: 3 ,
June 2001
Page(s): 506 -516

[\[Abstract\]](#) [\[PDF Full-Text \(244 KB\)\]](#) **IEEE JNL**

56 Optimal location of multi-type FACTS devices in a power system by means of genetic algorithms

Gerbex, S.; Cherkaoui, R.; Germond, A.J.;
Power Systems, IEEE Transactions on , Volume: 16 Issue: 3 , Aug.
2001
Page(s): 537 -544

[\[Abstract\]](#) [\[PDF Full-Text \(188 KB\)\]](#) **IEEE JNL**

57 Global transient stability and voltage regulation for power systems

Guo, Y.; Hill, D.J.; Wang, Y.;
Power Systems, IEEE Transactions on , Volume: 16 Issue: 4 , Nov.
2001
Page(s): 678 -688

[\[Abstract\]](#) [\[PDF Full-Text \(330 KB\)\]](#) **IEEE JNL**

58 A proposed of an AC/AC serial regulator using a capacitor as the serial component

de Oliveira, J.C.; Bissochi, C.A., Jr.; Vincenzi R.S., F.; Farias Luiz, V.J.; de Freitas, C.; Vieira, J.B., Jr.;
Applied Power Electronics Conference and Exposition, 2000. APEC
2000. Fifteenth Annual IEEE , Volume: 2 , 6-10 Feb. 2000
Page(s): 875 -879 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(244 KB\)\]](#) **IEEE CNF**

59 Low-thermal-resistance flip-chip fine package for 1-W voltage regulator IC

Takamura, F.; Tanaka, K.; Mitsuka, K.; Matsushita, H.;
Electronics Manufacturing Technology Symposium, 2000.
Twenty-Sixth IEEE/CPMT International , 2-3 Oct 2000
Page(s): 305 -310

[\[Abstract\]](#) [\[PDF Full-Text \(488 KB\)\]](#) **IEEE CNF**

60 An efficient single-switch voltage regulator

Vazquez, N.; Hernandez, C.; Cano, R.; Antonio, J.; Rodriguez, E.;
Arau, J.;
Power Electronics Specialists Conference, 2000. PESC 00. 2000 IEEE
31st Annual , Volume: 2 , 18-23 June 2000
Page(s): 811 -816 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(400 KB\)\]](#) **IEEE CNF**

61 Probabilistic evaluation of voltage stability in MV networks

Engels, K.; Haubrich, H.-J.;
Power Engineering Society Summer Meeting, 2000. IEEE , Volume: 4
, 16-20 July 2000
Page(s): 2075 -2080 vol. 4

[\[Abstract\]](#) [\[PDF Full-Text \(436 KB\)\]](#) **IEEE CNF**

62 Power system stabilizer tuning-simulations and commissioning

Wenyan Gu; Smulders, P.; Mushens, K.;
Power Engineering Society Summer Meeting, 2000. IEEE , Volume: 2
, 16-20 July 2000
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[\[Abstract\]](#) [\[PDF Full-Text \(292 KB\)\]](#) **IEEE CNF**

63 A prototype self-tuning adaptive power system stabilizer for damping of active power swings

Eichmann, A.; Kohler, A.; Malik, O.P.; Taborda, J.;
Power Engineering Society Summer Meeting, 2000. IEEE , Volume: 1
, 16-20 July 2000
Page(s): 122 -126 vol. 1

[\[Abstract\]](#) [\[PDF Full-Text \(416 KB\)\]](#) **IEEE CNF**

64 Studies on a multi-machine power system with a neural

network based excitation controller

Salem, M.M.; Zaki, A.M.; Mahgoub, O.A.; Abu El-Zahab, E.; Malik, O.P.;

Power Engineering Society Summer Meeting, 2000. IEEE , Volume: 1 , 16-20 July 2000

Page(s): 105 -110 vol. 1

[\[Abstract\]](#) [\[PDF Full-Text \(400 KB\)\]](#) **IEEE CNF**

65 A voltage quality improvement solution using step dynamic voltage regulator

Toh, C.W.; Lock, C.S.; Chen, S.; Choi, S.S.;

Power System Technology, 2000. Proceedings. PowerCon 2000.

International Conference on , Volume: 2 , 4-7 Dec. 2000

Page(s): 983 -988 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(428 KB\)\]](#) **IEEE CNF**

66 Analysis of RFI effects in voltage regulator ICs: measurement techniques, picking capability prediction and protection methods

Floean, D.; Pinelli, M.; Tomasin, P.;

Electromagnetic Compatibility, 2000. IEEE International Symposium on , Volume: 1 , 21-25 Aug. 2000

Page(s): 391 -395 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(320 KB\)\]](#) **IEEE CNF**

67 Investigation of voltage regulation stability of static synchronous compensator in power system

Li Chun; Jiang Qirong; Xu Jianxin;

Power Engineering Society Winter Meeting, 2000. IEEE , Volume: 4 , 23-27 Jan. 2000

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[\[Abstract\]](#) [\[PDF Full-Text \(540 KB\)\]](#) **IEEE CNF**

68 Fast voltage regulator for multilevel flash memories

Khouri, O.; Micheloni, R.; Gregori, S.; Torelli, G.;

Memory Technology, Design and Testing, 2000. Records of the 2000 IEEE International Workshop on , 7-8 Aug. 2000

Page(s): 34 -38

[\[Abstract\]](#) [\[PDF Full-Text \(348 KB\)\]](#) **IEEE CNF**

69 Adaptive critic based neurocontroller for turbogenerators with global dual heuristic programming

Venayagamoorthy, G.K.; Wunsch, D.C., II; Harley, R.G.;

Power Engineering Society Winter Meeting, 2000. IEEE , Volume: 1 , 23-27 Jan. 2000

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[\[Abstract\]](#) [\[PDF Full-Text \(348 KB\)\]](#) **IEEE CNF**

70 Automatic voltage regulator using an AC voltage-voltage converter

Hietpas, S.M.; Naden, M.;

Industry Applications, IEEE Transactions on , Volume: 36 Issue: 1 , Jan.-Feb. 2000

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[\[Abstract\]](#) [\[PDF Full-Text \(136 KB\)\]](#) **IEEE JNL**

71 A novel PWM scheme for single-phase three-level power-factor-correction circuit

Bor-Ren Lin; Hsin-Hung Lu;

Industrial Electronics, IEEE Transactions on , Volume: 47 Issue: 2 , April 2000

Page(s): 245 -252

[\[Abstract\]](#) [\[PDF Full-Text \(220 KB\)\]](#) **IEEE JNL**

72 Analysis of single-event transients in analog circuits

Adell, P.; Schrimpf, R.D.; Barnaby, H.J.; Marec, R.; Chatry, C.;

Calvel, P.; Barillot, C.; Mion, O.;

Nuclear Science, IEEE Transactions on , Volume: 47 Issue: 6 , Dec. 2000

Page(s): 2616 -2623

[\[Abstract\]](#) [\[PDF Full-Text \(170 KB\)\]](#) **IEEE JNL**

73 Investigation of candidate VRM topologies for future microprocessors

Xunwei Zhou; Pit-Leong Wong; Peng Xu; Lee, F.C.; Huang, A.Q.;

Power Electronics, IEEE Transactions on , Volume: 15 Issue: 6 , Nov 2000

Page(s): 1172 -1182

[\[Abstract\]](#) [\[PDF Full-Text \(400 KB\)\]](#) **IEEE JNL**

74 A particle swarm optimization for reactive power and voltage control considering voltage security assessment

Yoshida, H.; Kawata, K.; Fukuyama, Y.; Takayama, S.; Nakanishi, Y.;

Power Systems, IEEE Transactions on , Volume: 15 Issue: 4 , Nov. 2000

Page(s): 1232 -1239

[\[Abstract\]](#) [\[PDF Full-Text \(156 KB\)\]](#) **IEEE JNL**

75 Implementation of a fuzzy logic scheme for Q/V control in distribution systems

Ramakrishna, G.; Rao, N.D.;

Power Engineering Society 1999 Winter Meeting, IEEE , Volume: 2 , 31 Jan-4 Feb 1999

Page(s): 1316 -1321 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(700 KB\)\]](#) **IEEE CNF**

76 A novel frequency compensation technique for low-voltage low-dropout regulator

Ka Nang Leung; Mok, P.K.T.; Wing Hung Ki;

Circuits and Systems, 1999. ISCAS '99. Proceedings of the 1999 IEEE International Symposium on , Volume: 5 , 30 May-2 June 1999

Page(s): 102 -105 vol.5

[\[Abstract\]](#) [\[PDF Full-Text \(300 KB\)\]](#) **IEEE CNF**

77 A novel multilevel PWM control scheme of the AC/DC/AC converter for AC drives

Bor-Ren Lin; Hsin-Hung Lu;

Industrial Electronics, 1999. ISIE '99. Proceedings of the IEEE International Symposium on , Volume: 2 , 12-16 July 1999

Page(s): 795 -800 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(472 KB\)\]](#) **IEEE CNF**

78 Off-line identification of an open-loop automatic voltage regulator using pseudo-random binary sequence perturbations

Vermeulen, H.J.; Strauss, J.M.;

AFRICON, 1999 IEEE , Volume: 2 , 28 Sept.-1 Oct. 1999

Page(s): 799 -802 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(256 KB\)\]](#) **IEEE CNF**

79 Neurocontrol of turbogenerators with adaptive critic designs

Venayagamoorthy, G.K.; Wunsch, D.C., II; Harley, R.G.;
AFRICON, 1999 IEEE , Volume: 1 , 28 Sept.-1 Oct. 1999
Page(s): 489 -494 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(420 KB\)\]](#) [IEEE CNF](#)

80 Development of state space model and study of performance characteristics of digital based excitation control system ST4B with single machine connected to infinite bus

Rangnekar, S.;
Control Applications, 1999. Proceedings of the 1999 IEEE International Conference on , Volume: 2 , 22-27 Aug. 1999
Page(s): 1460 -1465 vol. 2

[\[Abstract\]](#) [\[PDF Full-Text \(256 KB\)\]](#) [IEEE CNF](#)

81 Modeling and design of three-phase systems using complex transfer functions

Gataric, S.; Garrigan, N.R.;
Power Electronics Specialists Conference, 1999. PESC 99. 30th Annual IEEE , Volume: 2 , 27 June-1 July 1999
Page(s): 691 -697 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(500 KB\)\]](#) [IEEE CNF](#)

82 Multilevel PWM for single-phase power factor pre-regulator

Bor-Ren Lin; Hsin-Hung Lu;
Circuits and Systems, 1999. ISCAS '99. Proceedings of the 1999 IEEE International Symposium on , Volume: 6 , 30 May-2 June 1999
Page(s): 57 -60 vol.6

[\[Abstract\]](#) [\[PDF Full-Text \(320 KB\)\]](#) [IEEE CNF](#)

83 Analysis of a voltage regulator for a two-phase induction motor drive

Popescu, M.; Demeter, E.; Micu, D.; Navrapescu, V.; Jokinen, T.;
Electric Machines and Drives, 1999. International Conference IEMD '99 , 9-12 May 1999
Page(s): 658 -660

[\[Abstract\]](#) [\[PDF Full-Text \(228 KB\)\]](#) [IEEE CNF](#)

84 Automatic voltage regulator using an AC voltage-voltage converter

Hietpas, S.M.; Naden, M.;

Rural Electric Power Conference, 1999 , 2-4 May 1999

Page(s): C5/1 -C5/7

[\[Abstract\]](#) [\[PDF Full-Text \(388 KB\)\]](#) **IEEE CNF**

85 Analysis of transformer inrush transients in offshore electrical systems

Smith, K.S.; Ran, L.; Leyman, B.;

Generation, Transmission and Distribution, IEE Proceedings- ,

Volume: 146 Issue: 1 , Jan. 1999

Page(s): 89 -95

[\[Abstract\]](#) [\[PDF Full-Text \(728 KB\)\]](#) **IEE JNL**

86 A new control scheme for single-phase PWM multilevel rectifier with power-factor correction

Bor-Ren Lin; Hsin-Hung Lu;

Industrial Electronics, IEEE Transactions on , Volume: 46 Issue: 4 , Aug. 1999

Page(s): 820 -829

[\[Abstract\]](#) [\[PDF Full-Text \(280 KB\)\]](#) **IEEE JNL**

87 Turbogenerator self-tuning automatic voltage regulator

Finch, J.W.; Zachariah, K.J.; Farsi, M.;

Energy Conversion, IEEE Transactions on , Volume: 14 Issue: 3 , Sept. 1999

Page(s): 843 -848

[\[Abstract\]](#) [\[PDF Full-Text \(420 KB\)\]](#) **IEEE JNL**

88 Intelligent control of a class of wind energy conversion systems

Chedid, R.; Mrad, F.; Basma, M.;

Energy Conversion, IEEE Transactions on , Volume: 14 Issue: 4 , Dec. 1999

Page(s): 1597 -1604

[\[Abstract\]](#) [\[PDF Full-Text \(508 KB\)\]](#) **IEEE JNL**

89 Power distribution system design methodology and

capacitor selection for modern CMOS technology

Smith, L.D.; Anderson, R.E.; Forehand, D.W.; Pelc, T.J.; Roy, T.;
Advanced Packaging, IEEE Transactions on [see also Components,
Packaging and Manufacturing Technology, Part B: Advanced
Packaging, IEEE Transactions on] , Volume: 22 Issue: 3 , Aug. 1999
Page(s): 284 -291

[\[Abstract\]](#) [\[PDF Full-Text \(204 KB\)\]](#) **IEEE JNL**

90 **Adaptive repetitive control of PWM inverters for very low
THD AC-voltage regulation with unknown loads**

Ying-Yu Tzou; Shih-Liang Jung; Hsin-Chung Yeh;
Power Electronics, IEEE Transactions on , Volume: 14 Issue: 5 , Sept.
1999
Page(s): 973 -981

[\[Abstract\]](#) [\[PDF Full-Text \(204 KB\)\]](#) **IEEE JNL**

91 **Application of a multivariable feedback linearization
scheme for rotor angle stability and voltage regulation of
power systems**

Akhrif, O.; Okou, F.-A.; Dessaint, L.-A.; Champagne, R.;
Power Systems, IEEE Transactions on , Volume: 14 Issue: 2 , May
1999
Page(s): 620 -628

[\[Abstract\]](#) [\[PDF Full-Text \(580 KB\)\]](#) **IEEE JNL**

92 **Design of a robust voltage controller for a buck-boost
converter using μ -synthesis**

Buso, S.;
Control Systems Technology, IEEE Transactions on , Volume: 7 Issue:
2 , March 1999
Page(s): 222 -229

[\[Abstract\]](#) [\[PDF Full-Text \(256 KB\)\]](#) **IEEE JNL**

93 **Volt/VAr control at distribution substations**

Baran, M.E.; Ming-Yung Hsu;
Power Systems, IEEE Transactions on , Volume: 14 Issue: 1 , Feb.
1999
Page(s): 312 -318

[\[Abstract\]](#) [\[PDF Full-Text \(692 KB\)\]](#) **IEEE JNL**

94 **Simulation studies with a continuously online trained artificial neural network controller for a micro-turbogenerator**

Venayagamoorthy, G.K.; Harley, R.G.;

Simulation '98. International Conference on (Conf. Publ. No. 457) ,
30 Sept.-2 Oct. 1998

Page(s): 405 -412

[\[Abstract\]](#) [\[PDF Full-Text \(556 KB\)\]](#) [IEEE CNF](#)

95 **Simulation of a 'direct on line' start of a large induction motor connected to a salient pole synchronous generator**

Tuinman, E.; Piers, P.; de Weerd, R.;

Simulation '98. International Conference on (Conf. Publ. No. 457) ,
30 Sept.-2 Oct. 1998

Page(s): 362 -367

[\[Abstract\]](#) [\[PDF Full-Text \(416 KB\)\]](#) [IEEE CNF](#)

96 **A novel AC voltage regulator**

El-Sabbe, A.; Zein El-Din, A.;

Industrial Electronics Society, 1998. IECON '98. Proceedings of the
24th Annual Conference of the IEEE , Volume: 2 , 31 Aug.-4 Sept.
1998

Page(s): 607 -611 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(360 KB\)\]](#) [IEEE CNF](#)

97 **Observer based power feedforward control for AC/AC converters**

Uhrin, R.; Zboray, L.; Profumo, F.;

Industry Applications Conference, 1998. Thirty-Third IAS Annual
Meeting. The 1998 IEEE , Volume: 3 , 12-15 Oct. 1998

Page(s): 1657 -1662 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(444 KB\)\]](#) [IEEE CNF](#)

98 **Analysis of large turbogenerator's asynchronous operation during loss of field**

Guo, K.Z.; Zhu, W.D.; Tan, F.W.; Jin, R.L.; Wang, G.;

Power System Technology, 1998. Proceedings. POWERCON '98. 1998
International Conference on , Volume: 2 , 18-21 Aug. 1998

Page(s): 935 -940 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(380 KB\)\]](#) [IEEE CNF](#)

99 **Design of an H^∞ robust controlled single-stage converter with high power factor and fast regulation**

Wu, T.-F.; Chen, Y.-K.;

Power Electronics Specialists Conference, 1998. PESC 98 Record.
29th Annual IEEE , Volume: 1 , 17-22 May 1998

Page(s): 315 -321 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(512 KB\)\]](#) **IEEE CNF**

100 **A practical continually online trained artificial neural network controller for a turbogenerator**

Venayagamoorthy, G.K.; Harley, R.G.;

Industrial Electronics, 1998. Proceedings. ISIE '98. IEEE International Symposium on , Volume: 2 , 7-10 July 1998

Page(s): 385 -389 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(360 KB\)\]](#) **IEEE CNF**

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